

+

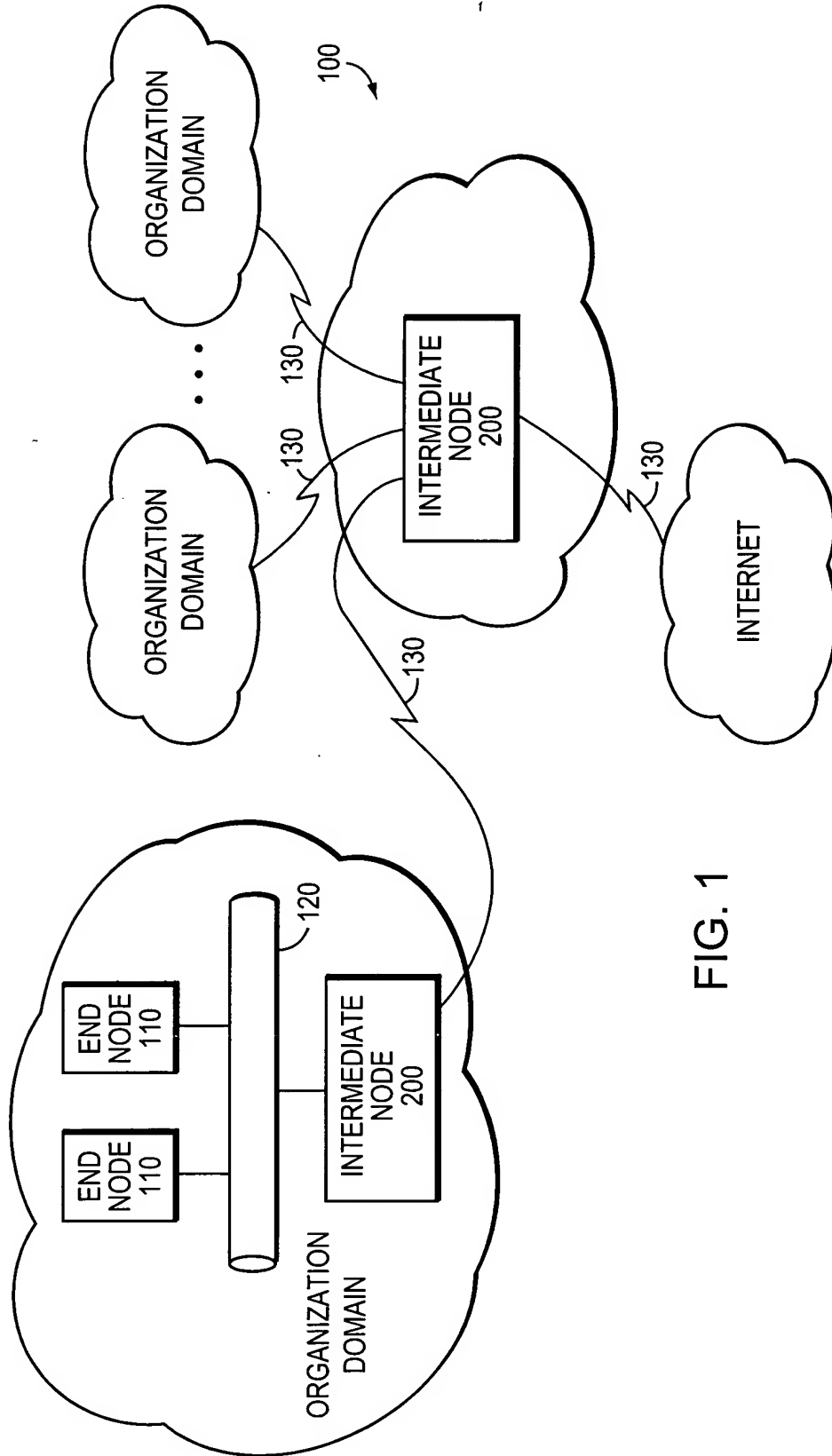


FIG. 1

+

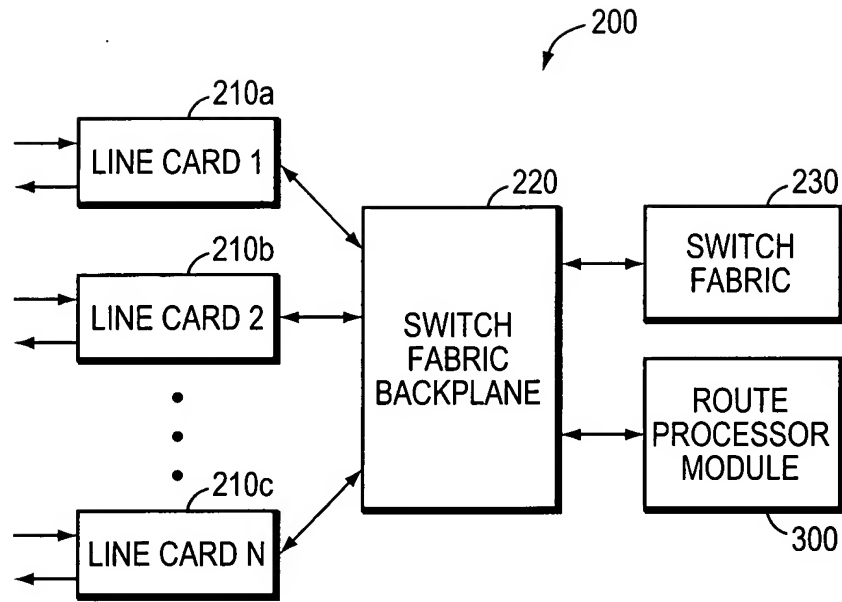


FIG. 2

10035509 1.02801

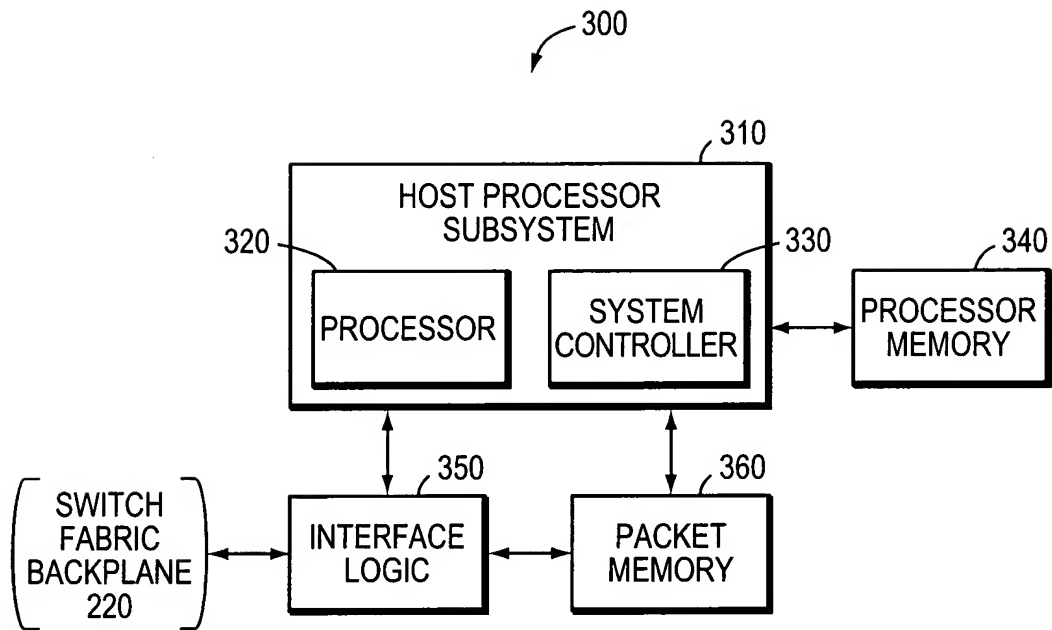


FIG. 3

FIG. 3 is a block diagram of a system architecture.

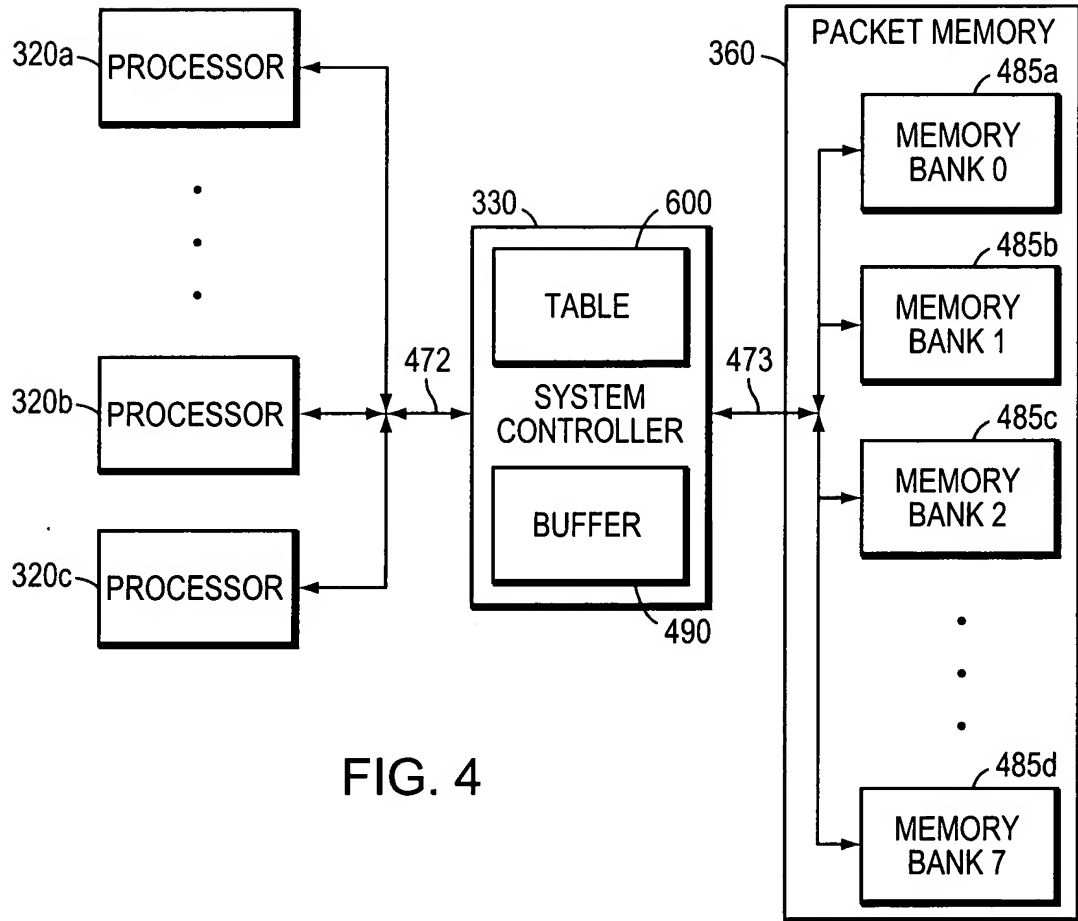


FIG. 4

FIG. 4 is a block diagram of a system architecture.

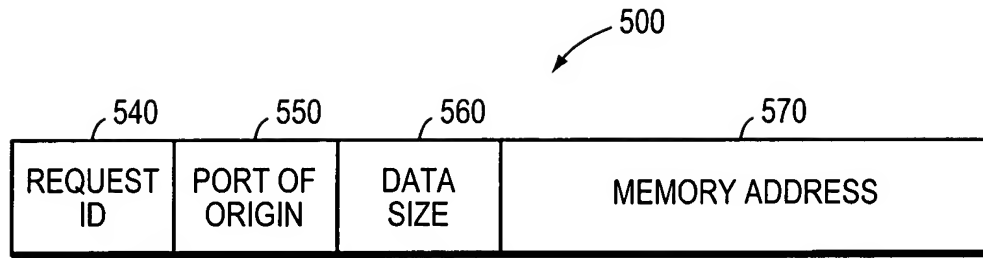


FIG. 5

10036609 132804

600

640	650	660	670	61
REQUEST ID	PORT OF ORIGIN	DATA SIZE	MEMORY ADDRESS	
REQUEST ID	PORT OF ORIGIN	DATA SIZE	MEMORY ADDRESS	
REQUEST ID	PORT OF ORIGIN	DATA SIZE	MEMORY ADDRESS	

FIG. 6